

Vivek V Kajagar
At/post:Tallur,Tq:Saundatti,Dist:Belgaum
Karnataka-591117-India.
Mobile no.: +91-8861787988,7892192844
E-mail: vivekvkajagar5@gmail.com



Curriculum Vitae

Personal data:

Date of Birth : 30.07.1989
Nationality : Indian
Family status : Married.
Languages : English (Excellent), Kannada (Mother tongue), Hindi & Telugu

Academic qualification:

Master in VLSI Design & Embedded systems Nov. 2013
T-John Institute of Technology Bangalore -Vishveshwarya Technological University India.

Bachelor in electronics and communication engineering Sep. 2011
Dr.P.G.H College of engineering and technology-Vishveshwarya Technological University,
India.

Experience

- 6 years of experience in Electronic, Embedded systems (Industrial) and Teaching.
- **Asst. Prof.** July.2019 - up to date
Jain College of Engineering & Research-Belgaum
 - **Asst. Prof.** Nov. 2016 - July.2019
Madanapalle Institute of Technology & Sciences (Autonomous) India
 - **Embedded software developer** Sep.2014 - Nov.2016
Pragmatic Embedded Solutions-Bangalore India
-

Other related experience related to industrial, teaching and research

- Experience in teaching, conducting practical training sessions, organizing workshops for graduate (B.Tech) and post graduate (M.Tech) students.
 - Supervise students with Microprocessor lab, Embedded lab, Digital Design lab & DSP lab.
 - Supervise undergraduate students and graduate student with their project work.
-
-

Technical Skills:

- Languages: C, Embedded C, VHDL, Assembly, Matlab
- PCB designing tools: Eagle, Orcad
- VLSI tools: Active HDL, Xilinx ISE 9.1, Cadence
- Software tools: Matlab, EMU8086
- Microcontrollers: Renesas
RL78/G12/G13, PIC18F458, P89V51RD2, LPC2148/2129/176s8, MSP430.
- Embedded system tools: Kiel μ vision 3/4/5, flash programmer, Proteus, ESA 86\88E.Masm, Office tools: Microsoft office, one note,

Extracurricular activities:

- Participated in National Level Technical Fest (Pleiades'2010). Held in B.V. Bhoomaraddi College of Engg. & Tech Hubli.
- Participated Tech Fest (Think, Create Engineer'2010), Held in National Institute of Technology Karnataka Suratkal.
- Worked as workshop coordinator for MSP430 microcontroller, which was held in Madanapalle institute of technology-Madanapalle.
- Attended "Cyber security and applications" one-day seminar, which was held in Madanapalle institute of technology-Madanapalle.
- Presented paper in "Fourth International Symposium on Signal Processing and Intelligent Recognition Systems (SIRS'18)" held in PES Institute of Technology Bangalore.
- NPTEL online certification & FDP in "Cmos Digital Vlsi Design".

Publications & Conferences:

1. Vivek V Kajagar, Shaik Mohammad Ashraf Ansari, J N Swaminathan and Radhakrishnan Subramanian, "Quality and Complexity measurement of 2D-DCT architecture using loeffler algorithm along with CSD and CSE" Springer, Communications in Computer and Information Science journal, ISSN 1865-0929, 2018.
2. Vivek V Kajagar, Shaik Mohammad Ashraf Ansari, R Kiran Kumar" Comparative Analysis of cmos Domino Logic of AND Gate Using Different Nano Scaling Technologies" Third IEEE International Conference on Electrical, Computer and Communication Technologies (IEEE ICECCT 2019).
3. R Kiran Kumar, Vivek V Kajagar, Shaik Mohammad Ashraf Ansari and G Charan Kumar, "A study on Reconfigurable UWB Microstrip Antenna using HFSS" Third IEEE International Conference on Electrical, Computer and Communication Technologies (submitted).

I am vivek. Kajagar, hereby declare the above furnished information is authentic to best of my knowledge.

Place:

Date:

(Vivek V.Kajagar)