


Faculty Name	Prof. Nivedita Shettar	
Department	Electronics & Communication Engineering	
Specialization	VLSI Design and Testing	
Experience in Years	07 Years	
Area of Interest	VLSI Design, SOC and IOT	
E-mail Id	niveditashettar@gmail.com	
Contact Number	+91- 7829041568	

Seminar/Workshop Attended:

Sl. No.	Nature of the course	Organizing Agency	Place	No. of Days
1	Workshop	BVBCET	Hubli	2
2	Workshop	BVBCET	Hubli	1
3	ISTE Workshop	IIT Kharagpur at GIT	Belagavi	2 weeks
4	Workshop	BVBCET	Hubli	1
5	Workshop	BVBCET	Hubli	1
6	Workshop	BVBCET	Hubli	2
7	FDP	KLE Dr. M S Sheshgiri	Belagavi	4
8	Coursera	University of California, Irvine		2 week
9	Workshop(Webinar)	AICTE		5
10	FDP	KLE Dr. M S Sheshgiri	Belagavi	3

Paper Publications:

Papers published in National Conferences:

- i) “Design of Arbiter for DDR2 Memory Controller and Interfacing Frontend with the Memory through Backend ”submitted in IEEE International Conference on Electrical ,Electronics and Optimization Techniques(ICEEOT) Assessment – December ,2015.
- ii) Presented paper on “Modeling of Bandwidth Efficient Multiple data Acquisition Embedded System” at the National Conference on Current trends in Industrial Automation” held at BVBCET, Hubli.

Papers published in International / National journals: NIL

Internship/Conference attended:

1. National Conference on Current trends in Industrial Automation” held at BVBCET, Hubli.

Recognized State/National/International Projects:

1. The project guided “ Multi Operational Smart Vehicle” was selected for KSCST.